

SEMESTER S1/S2
BASIC ELECTRICAL AND ELECTRONICS ENGINEERING
WORKSHOP

(Common to All Groups except for Civil Engineering Branch)

Course Code	GXESL106	CIE Marks	50
Teaching Hours/Week (L: T:P: R)	0:0:2:0	ESE Marks (Internal only)	50
Credits	1	Exam Hours	2 Hrs. 30 Min.
Prerequisites (if any)	None	Course Type	Lab

Course Objectives:

1. To create awareness and familiarity with electrical wiring and safety measures to be taken.
2. To Identify various electronic components and to operate various measuring instruments
3. Learn to setup simple electronic circuits on breadboard and PCB

Expt. No.	Experiments
Electrical Workshop (Minimum of 7 Experiments to be done)	
1	a) Demonstrate the precautionary steps adopted in case of Electrical shocks. b) Identify different types of cables, wires, switches, fuses, fuse carriers, MCB, ELCB and MCCB, familiarize the ratings.
2	Wiring of a simple light circuit for light/ fan point (PVC conduit wiring) and a 6A plug socket with individual control.
3	Wiring of light/fan circuit using two-way switches. (Staircase wiring)
4	Wiring of fluorescent lamp and a power plug (16 A) socket with a control switch.
5	Wiring of power distribution arrangement using single phase MCB distribution board with ELCB, main switch and Energy meter.
6	Familiarisation of step up and step-down transformers, (use low voltage transformers) Measurement and representation of voltage and waveform to scale in graph sheet with the help of CRO
7	Familiarisation of rheostats, measurement of potential across resistance elements and introducing the concept of relative potential using a DC circuit.

8	<p>a) Identify battery specifications using different types of batteries. (Lead acid, Li Ion, NiCd etc.)</p> <p>b) Familiarize different types of earthing (Pipe, Plate Earthing, Mat Schemes) and ground enhancing materials (GEM).</p>
<p>ELECTRONICS WORKSHOP</p> <p>(Minimum of 7 Experiments to be done)</p>	
1	Familiarization/Identification of electronic components with specification (Functionality, type, size, colour coding, package, symbol and cost of -Active, Passive, Electrical, Electronic, Electro-mechanical, Wires, Cables, Connectors, Fuses, Switches, Relays, Crystals, Displays, Fasteners, Heat sink etc.)
2	Drawing of electronic circuit diagrams using BIS/IEEE symbols and Interpret data sheets of discrete components and IC's
3	Familiarization/Application of testing instruments and commonly used tools. - Multimeter, Function generator, Power supply, CRO, DSO. Soldering iron, Desoldering pump, Pliers, Cutters, Wire strippers, Screw drivers, Tweezers, Crimping tool, Hot air soldering and de- soldering station
4	Testing of electronic components using multimeter - Resistor, Capacitor, Diode, Transistor and JFET.
5	Printed circuit boards (PCB) - Types, Single sided, Double sided, PTH, Processing methods. Design and fabrication of a single sided PCB for a simple circuit.
6	Inter-connection methods and soldering practice. Bread board, Wrapping, Crimping, Soldering - types - selection of materials and safety precautions. Soldering practice in connectors and general-purpose PCB, Crimping.

7	Assembling of electronic circuit/system on general purpose PCB, test and show the functioning (Any two)- Fixed voltage power supply with transformer <ul style="list-style-type: none"> • Rectifier diode • Capacitor filter • Zener/IC regulator Square wave generation using IC 555 timer in IC base.
8	Assembling of electronic circuits using SMT (Surface Mount Technology) stations.
9	Introduction to EDA tools (such as KiCad or Xcircuit)

**Course Assessment Method
(CIE: 50 marks, ESE: 50 marks)**

Continuous Internal Evaluation Marks (CIE):

Attendance	Preparation/Pre-Lab Work, experiments, Viva and Timely completion of Lab Reports / Record (Continuous Assessment)	Total
5	45	50

End Semester Examination Marks (ESE): (Internal evaluation only)

Procedure/ Preparatory work/Design/ Algorithm	Conduct of experiment/ Execution of work/ troubleshooting/ Programming	Result with valid inference/ Quality of Output	Viva voce	Record	Total
10	15	10	10	5	50

- Submission of Record: Students shall be allowed for the end semester examination only upon submitting the duly certified record.
- Minimum Pass Mark: The requirement for passing the lab course included in the first-year curriculum is that the student must score a minimum of 50% overall, combining marks from both Continuous Internal Evaluation (CIE) and End Semester Examination (ESE). There is no separate minimum requirement for each component.
- There will not be any relaxation in the attendance requirement.

Course Outcomes (COs)

At the end of the course students should be able to:

	Course Outcome	Bloom's Knowledge Level (KL)
CO1	Demonstrate safety measures against electrical shocks	K2
CO2	Familiarise with transformers, rheostats, batteries and earthing schemes	K2
CO3	Illustrate the connection diagram and identify the suitable accessories necessary for wiring simple electric circuits	K3
CO4	Identify various electronic components	K2
CO5	Operate various measuring instruments	K3
CO6	Apply the design procedure of simple electronic circuits on breadboard and PCB	K3
CO7	Build the ability to work in a team with good interpersonal skills	K3

Note: K1- Remember, K2- Understand, K3- Apply, K4- Analyse, K5- Evaluate, K6- Create

CO-PO Mapping Table:

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1						3						2
CO2	1					2	1					2
CO3	2					1						2
CO4	3					2						3
CO5	3				3	2			2			3
CO6	3		3	1	3	2	1		2			3
CO7									3	2		2

Text Books				
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	Electrical Design Estimating and Costing	K B Raina and S KBhattacharya	New Age International Publishers	2/e 2024
2	Electrical Systems Design	M K Giridharan	I K International Publishing House Pvt. Ltd	3/e 2022
3	Basic Electrical Engineering	D P Kothari and I J Nagrath	Tata McGraw Hill	4/e 2019
4	Basic Electronics and Linear Circuits	NN Bhargava, D C Kulshreshtha and S C Gupta	Mc Graw Hill	2/e 2017

Continuous Assessment with equal weightage for both specializations (45 Marks)

1. Preparation and Pre-Lab Work (10 Marks)

- Pre-Lab Assignments: Assessment of pre-lab assignments or quizzes that test understanding of the upcoming experiment.
- Skill Proficiency: Proficiency in handling equipment, accuracy in observations, and troubleshooting skills during the experiments.
- Teamwork: Collaboration and participation in group experiments.

2. Lab Reports and Record Keeping (10 Marks)

- Quality of Reports: Clarity, completeness and accuracy of lab reports. Proper documentation of experiments, data analysis and conclusions.
- Timely Submission: Adhering to deadlines for submitting lab reports/rough record and maintaining a well-organized fair record.

3. Viva Voce (10 Marks)

- Oral Examination: Ability to explain the experiment, results and underlying principles during a viva voce session.

Evaluation Pattern for End Semester Examination with equal weightage in both specializations (50 Marks)

1. Procedure/Preliminary Work/Design/Algorithm (10 Marks)

- Procedure Understanding and Description: Clarity in explaining the procedure and understanding each step involved.
- Preliminary Work and Planning: Thoroughness in planning and organizing materials/equipment.
- Algorithm Development: Correctness and efficiency of the algorithm related to the experiment.
- Creativity and logic in algorithm or experimental design.

2. Conduct of Experiment/Execution of Work/Programming (15 Marks)

- Setup and Execution: Proper setup and accurate execution of the experiment or programming task.

3. Result with Valid Inference/Quality of Output (10 Marks)

- Accuracy of Results: Precision and correctness of the obtained results.
- Analysis and Interpretation: Validity of inferences drawn from the experiment or quality of program output.

4. Viva Voce (10 Marks)

- Ability to explain the experiment, procedure results and answer related questions
- Proficiency in answering questions related to theoretical and practical aspects of the subject.

5. Record (5 Marks)

- Completeness, clarity, and accuracy of the lab record submitted